

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Masato MORI et al. **Mail Stop PCT**
Confirmation No. 8311
Appl. No: : 10/597,949 (National Stage of PCT/JP2005/003043)
I. A. Filed : February 24, 2005
For : ELECTRONIC COMPONENT MOUNTING METHOD, AND CIRCUIT
SUBSTRATE AND CIRCUIT SUBSTRATE UNIT USED IN THE
METHOD

COVER LETTER

Mail Stop PCT
Commissioner for Patents
U.S. Patent and Trademark Office
Customer Service Window, Mail Stop PCT
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Sir:

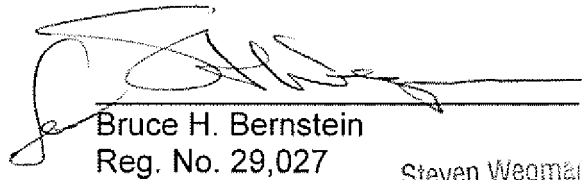
In order to comply with the requirements for the submission of an executed Declaration in the above-identified application, and prior to receipt of the Notification which should set a time limit for submission of materials, and further to the filing of the above-identified application and the filing fee on August 14, 2006, Applicant hereby submits:

- Transmittal Letter to the United States Designated/Elected Office Concerning a Filing Under 35 U.S.C. 371;
- an executed Declaration and Power of Attorney;
- a payment in the amount of \$130.00 as payment of the surcharge for late filing of the Declaration.

The U.S. Patent and Trademark Office is hereby authorized to charge any additional fees or credit any overpayment to Deposit Account No. 19-0089.

Should the Examiner have any questions, the Examiner is invited to contact the undersigned at the below-listed number.

Respectfully submitted,
Masato MORI et al.



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